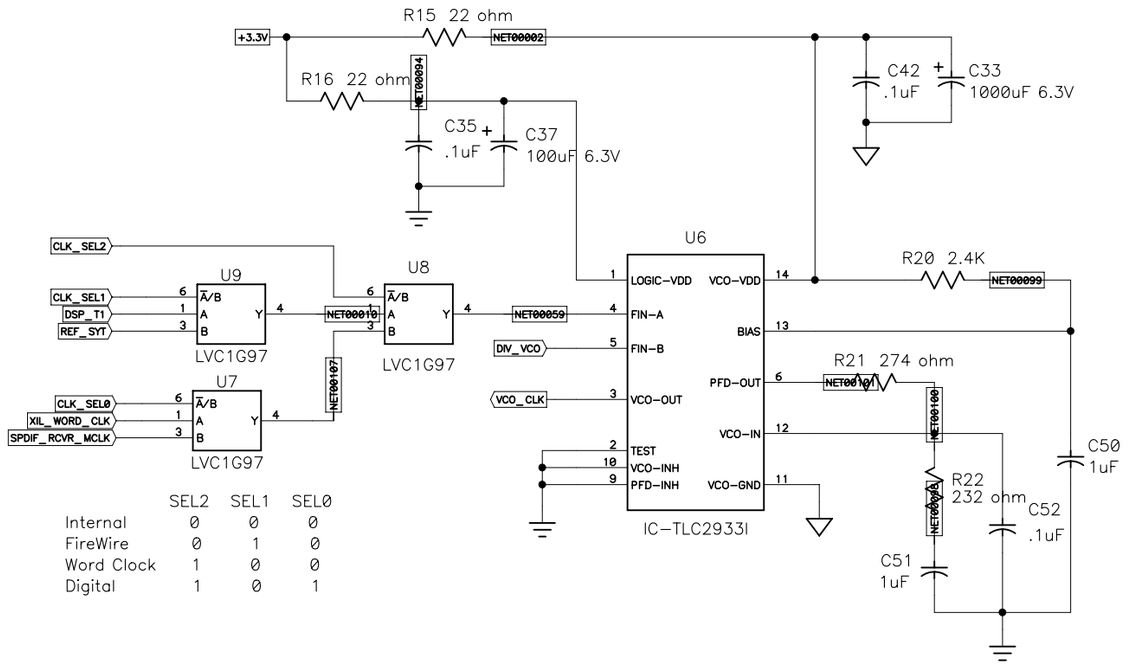
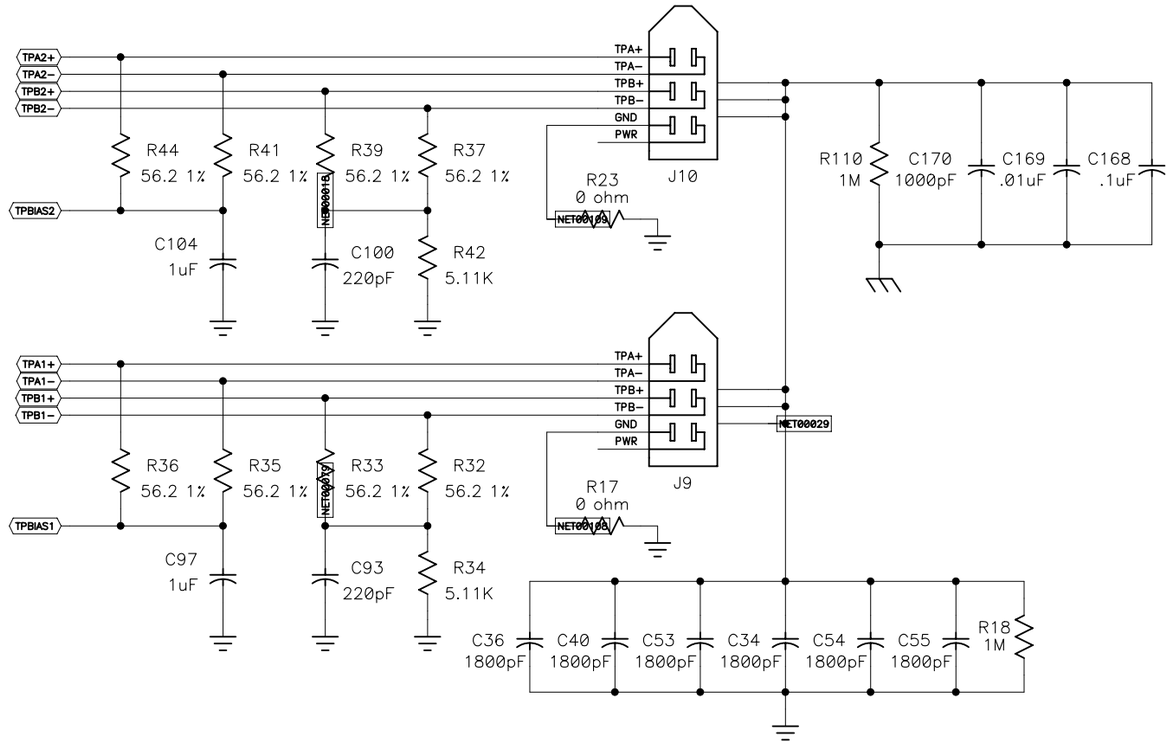
1394 PHY, PLL, AND CLOCKS

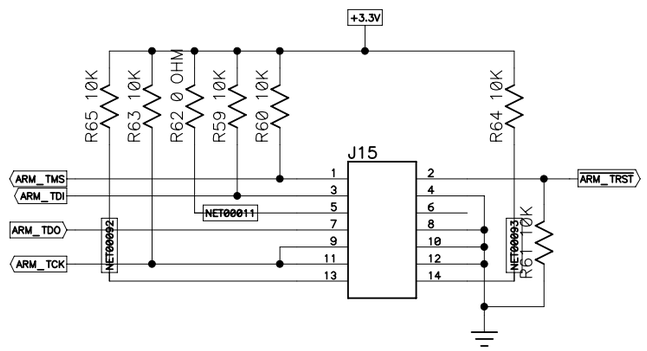
CLOCK RECOVERY VCO



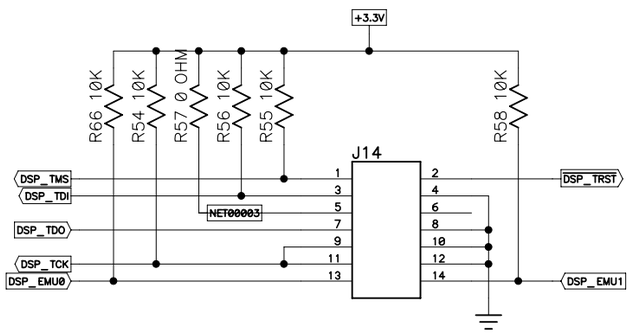
	SEL2	SEL1	SEL0
Internal	0	0	0
FireWire	0	1	0
Word Clock	1	0	0
Digital	1	0	1



ARM JTAG



DSP JTAG



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U10

IC–TSB43CB43

HIGH AT RESET FOR LITTLE ENDIAN

TYPE 1 SH3 STYLE ACCESS
(0,0,1)

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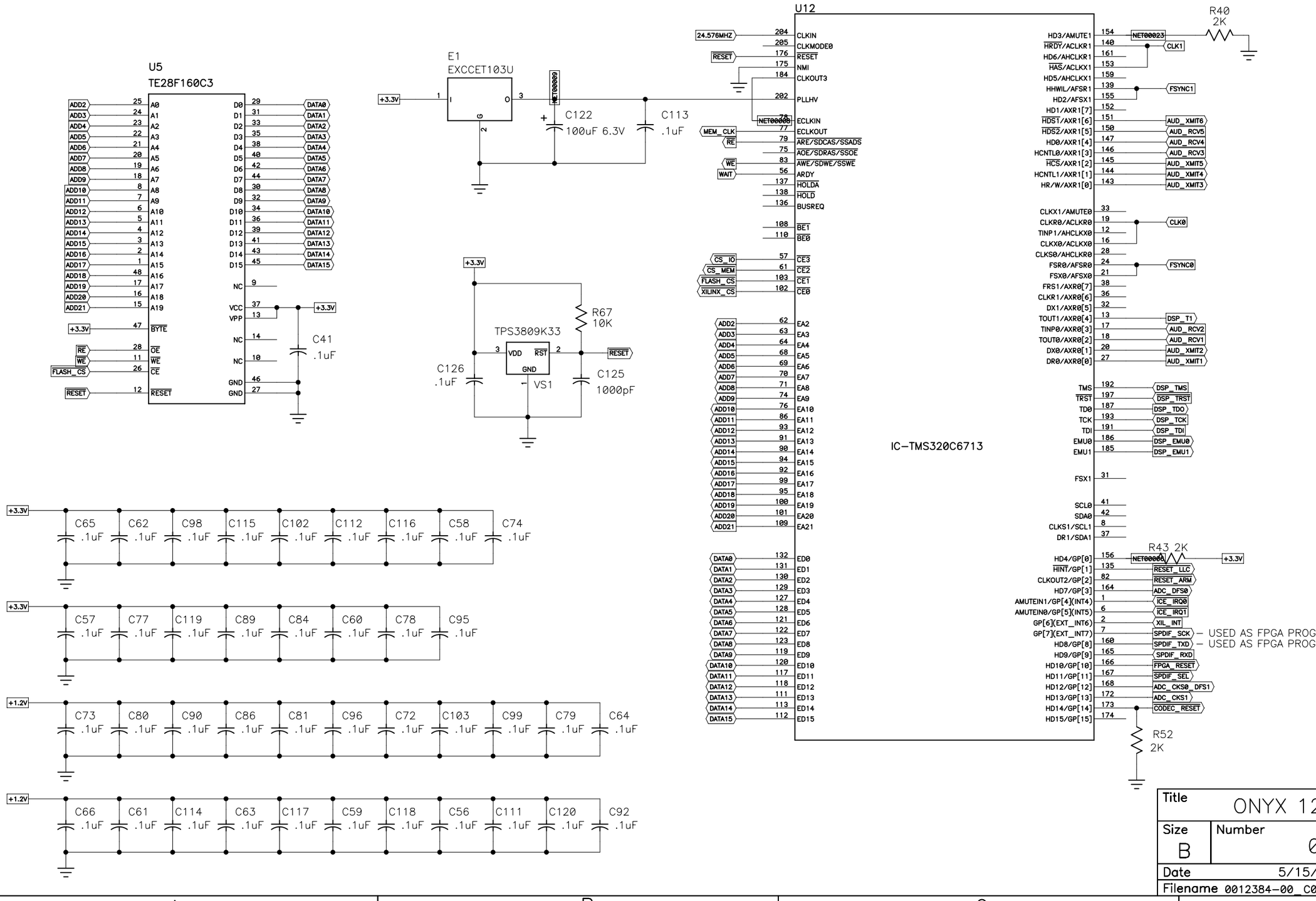

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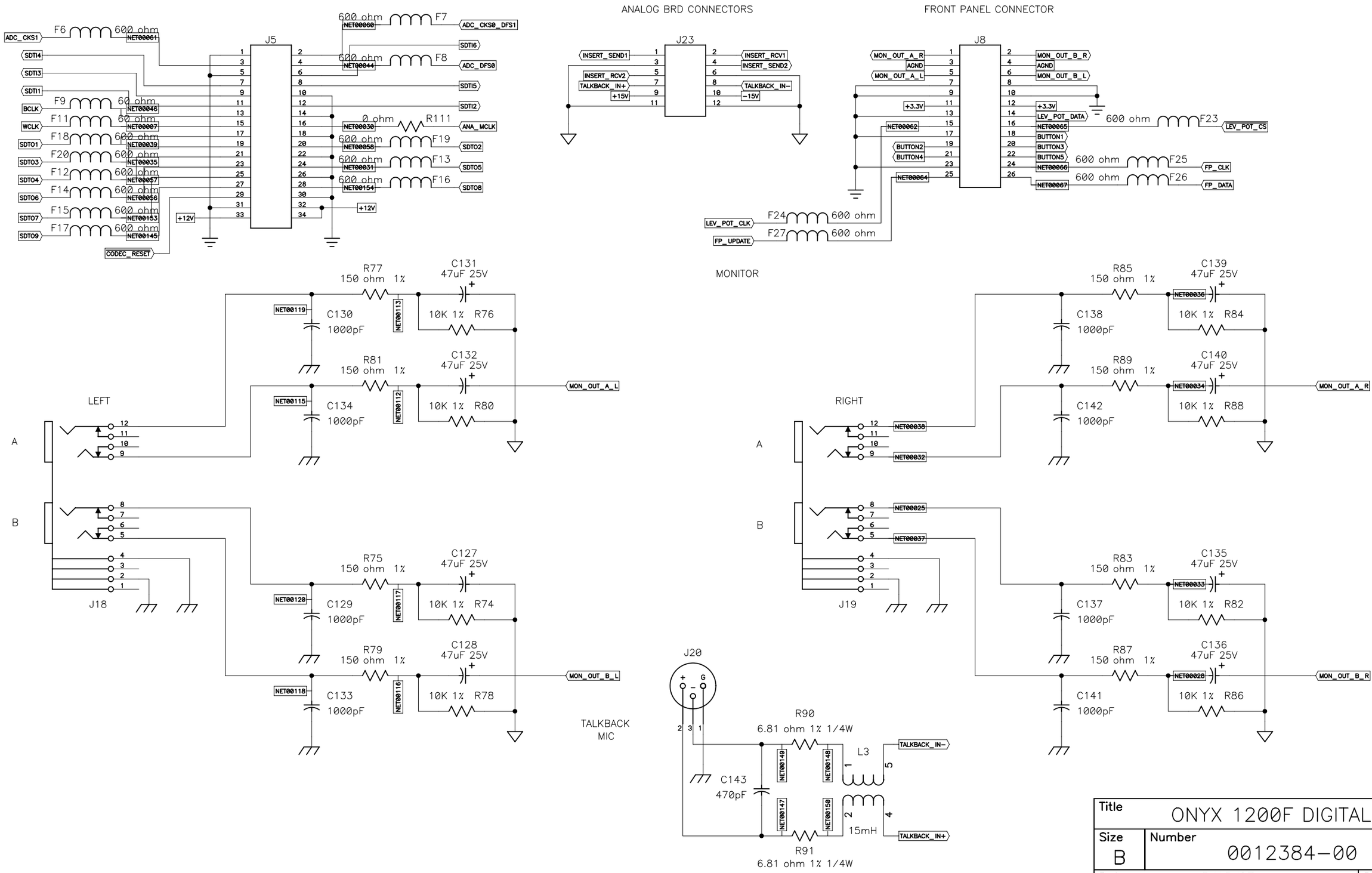

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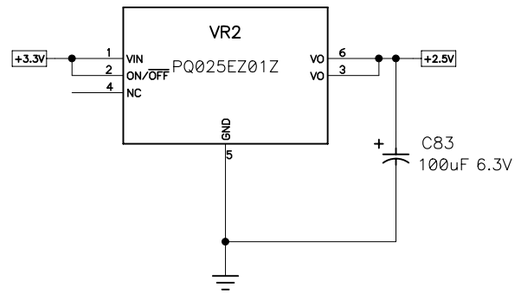

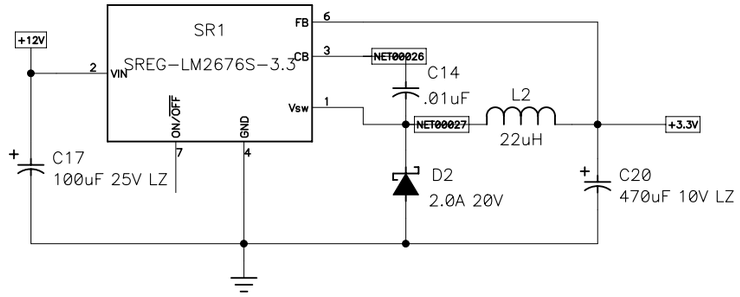

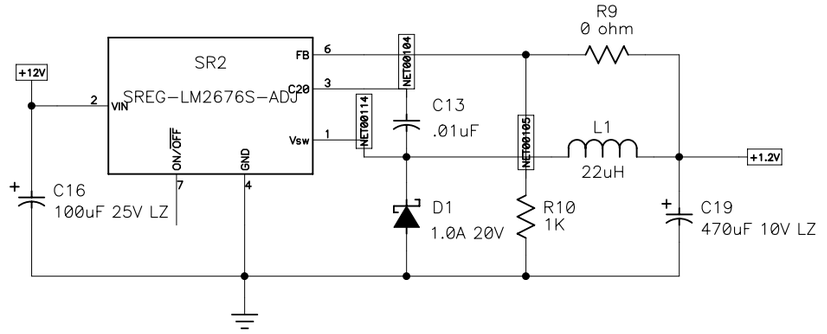

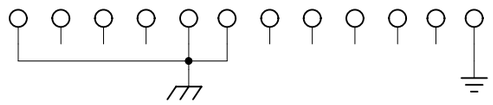

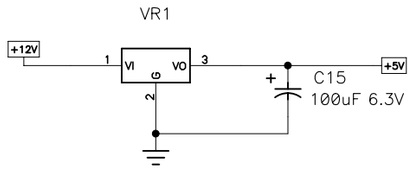

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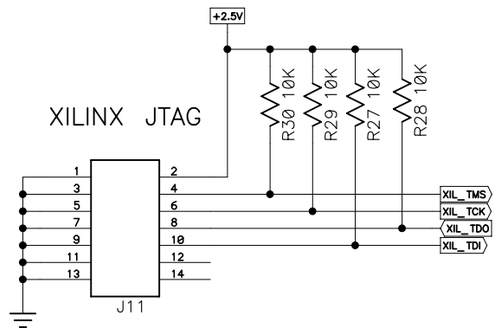

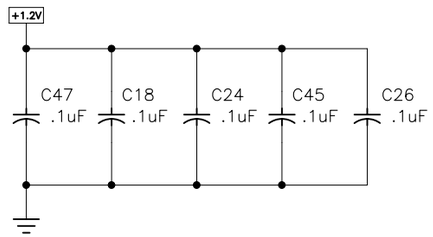

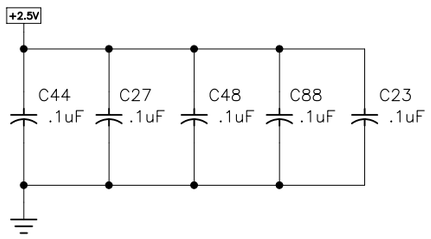

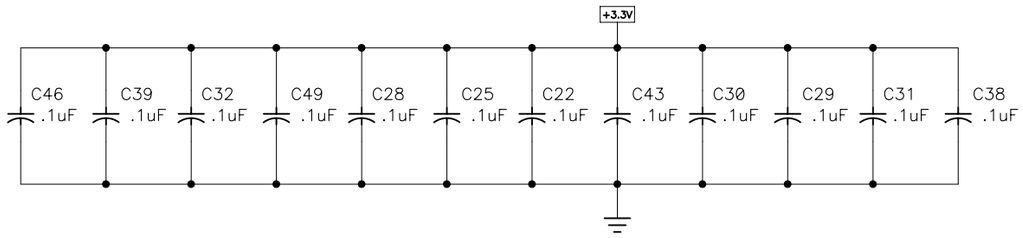

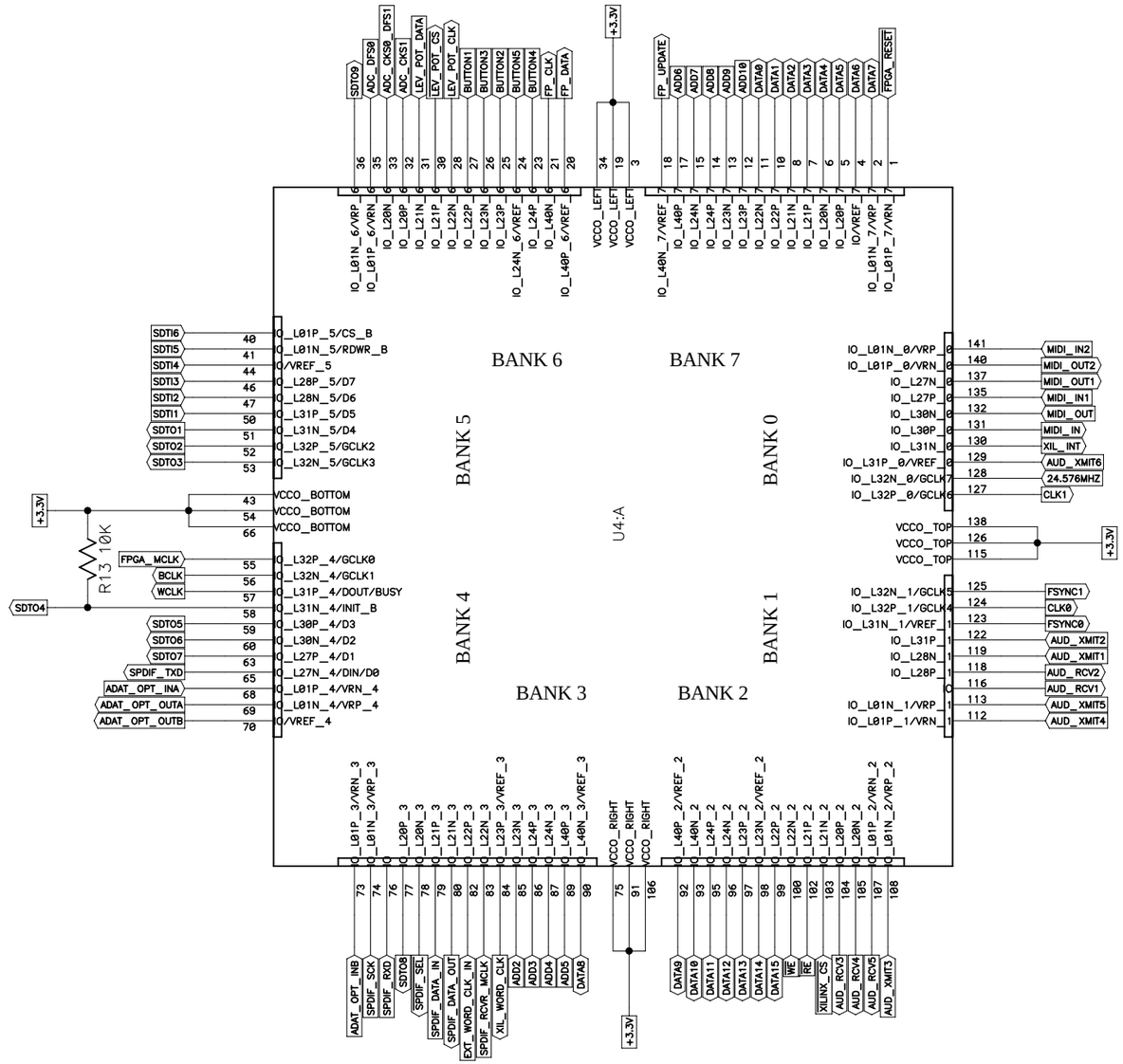

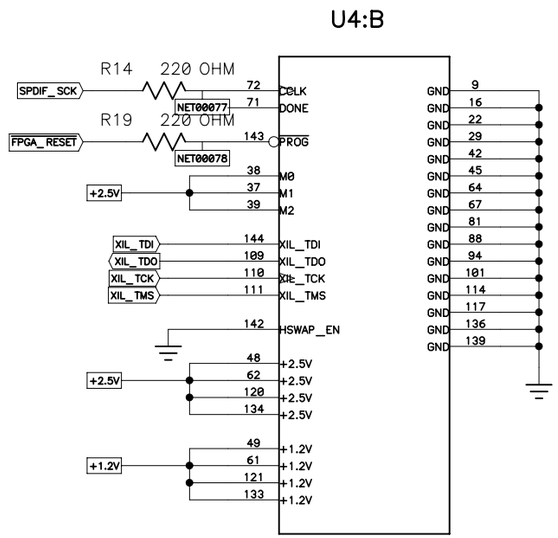

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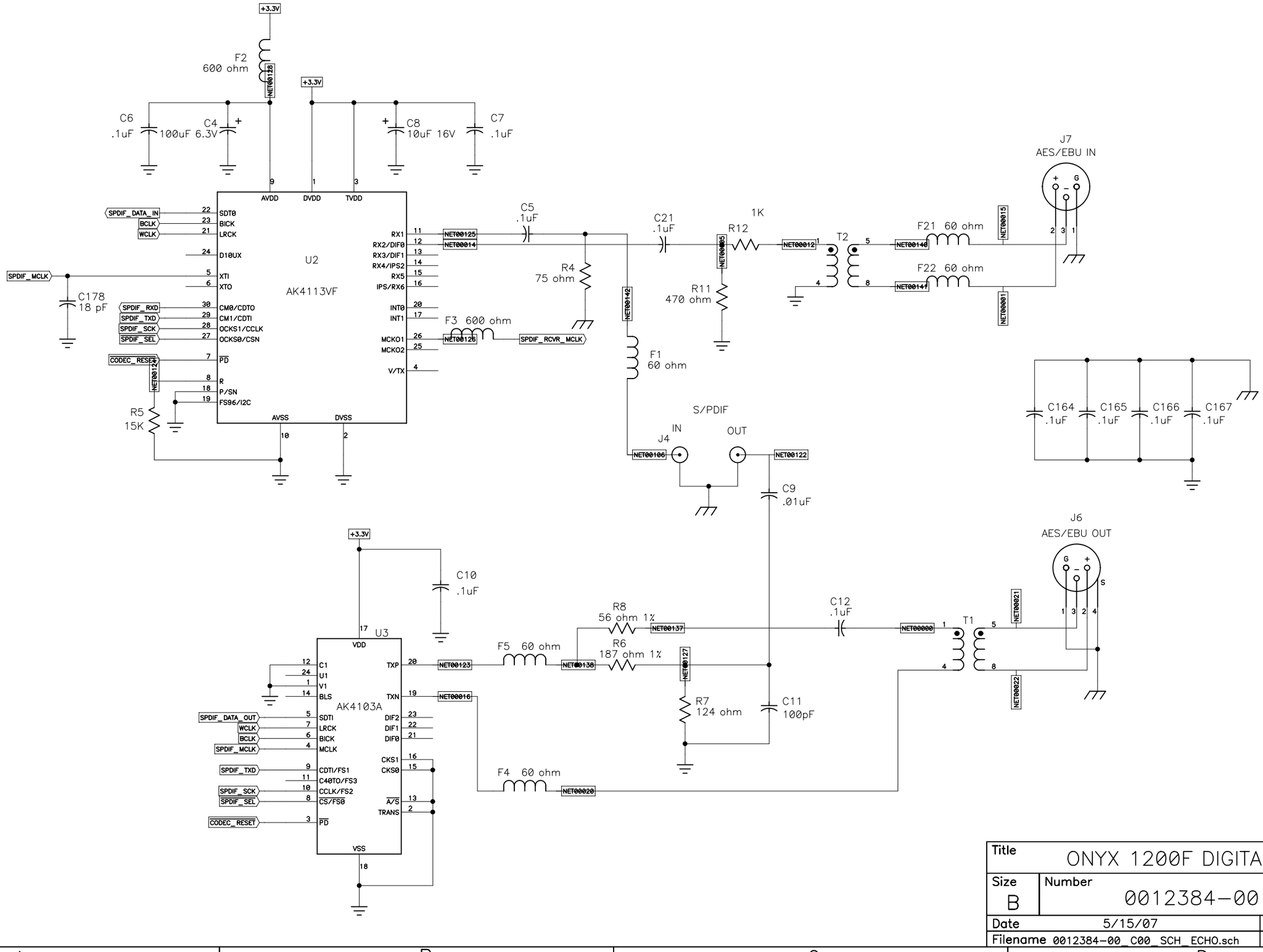

Title	ONYX 1200F DIGITAL BOARD
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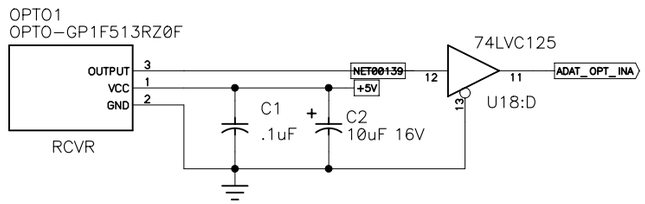

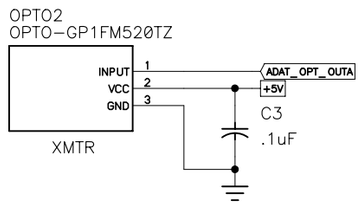

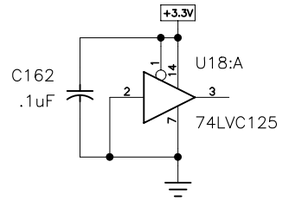

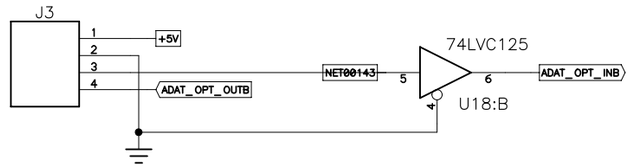

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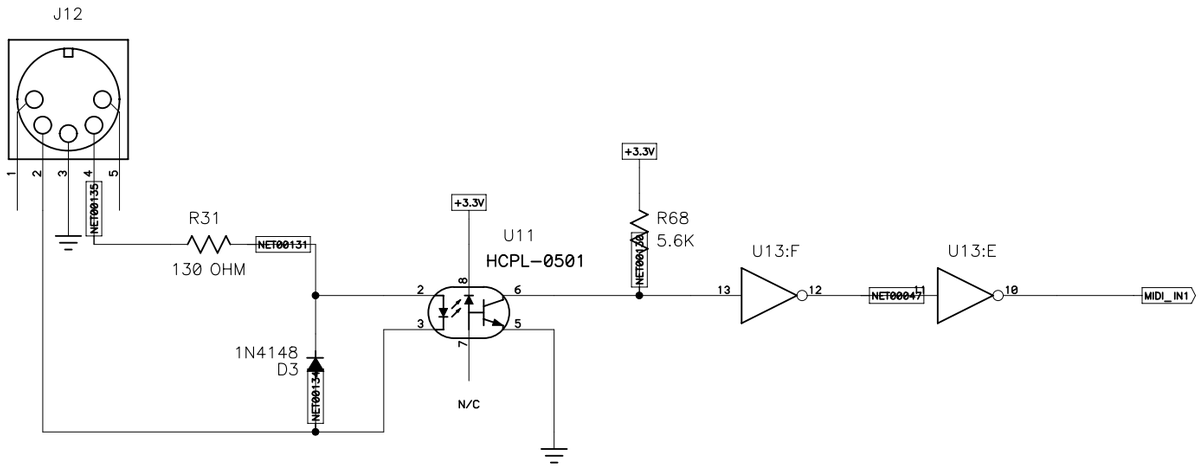

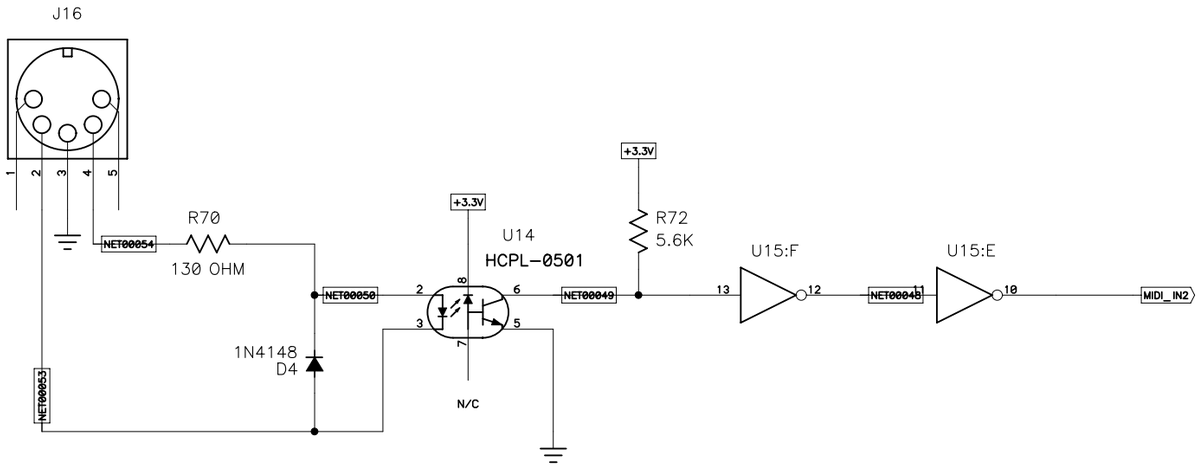

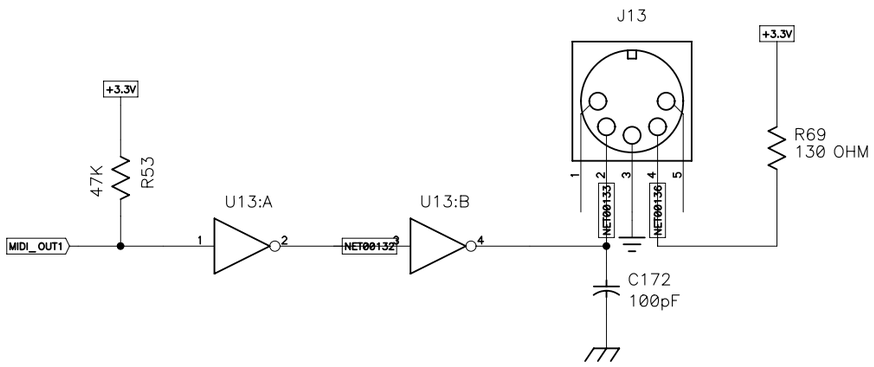

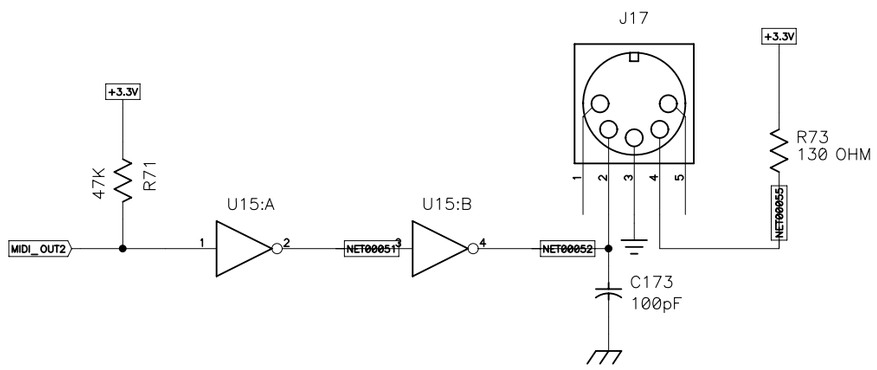

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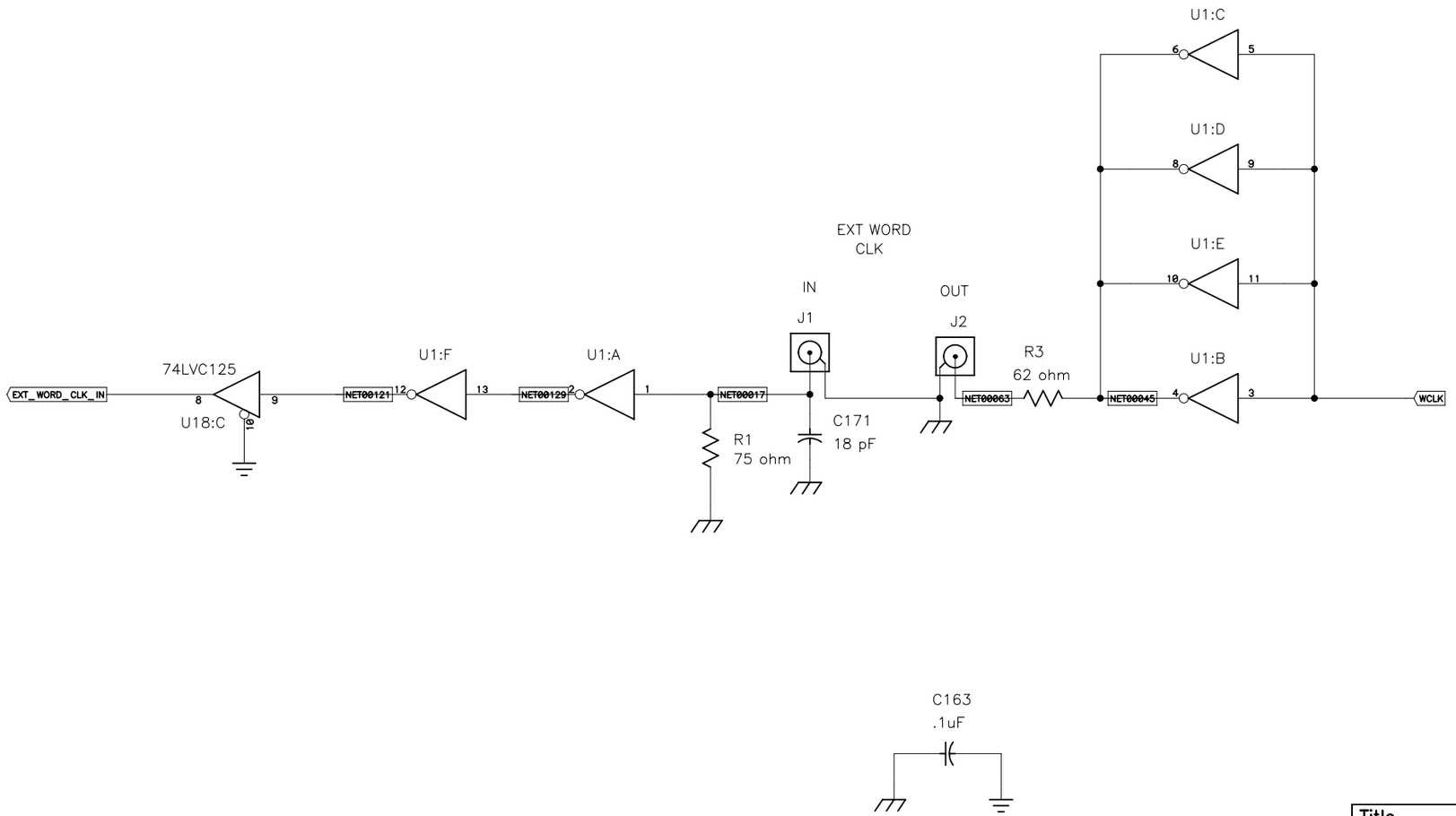

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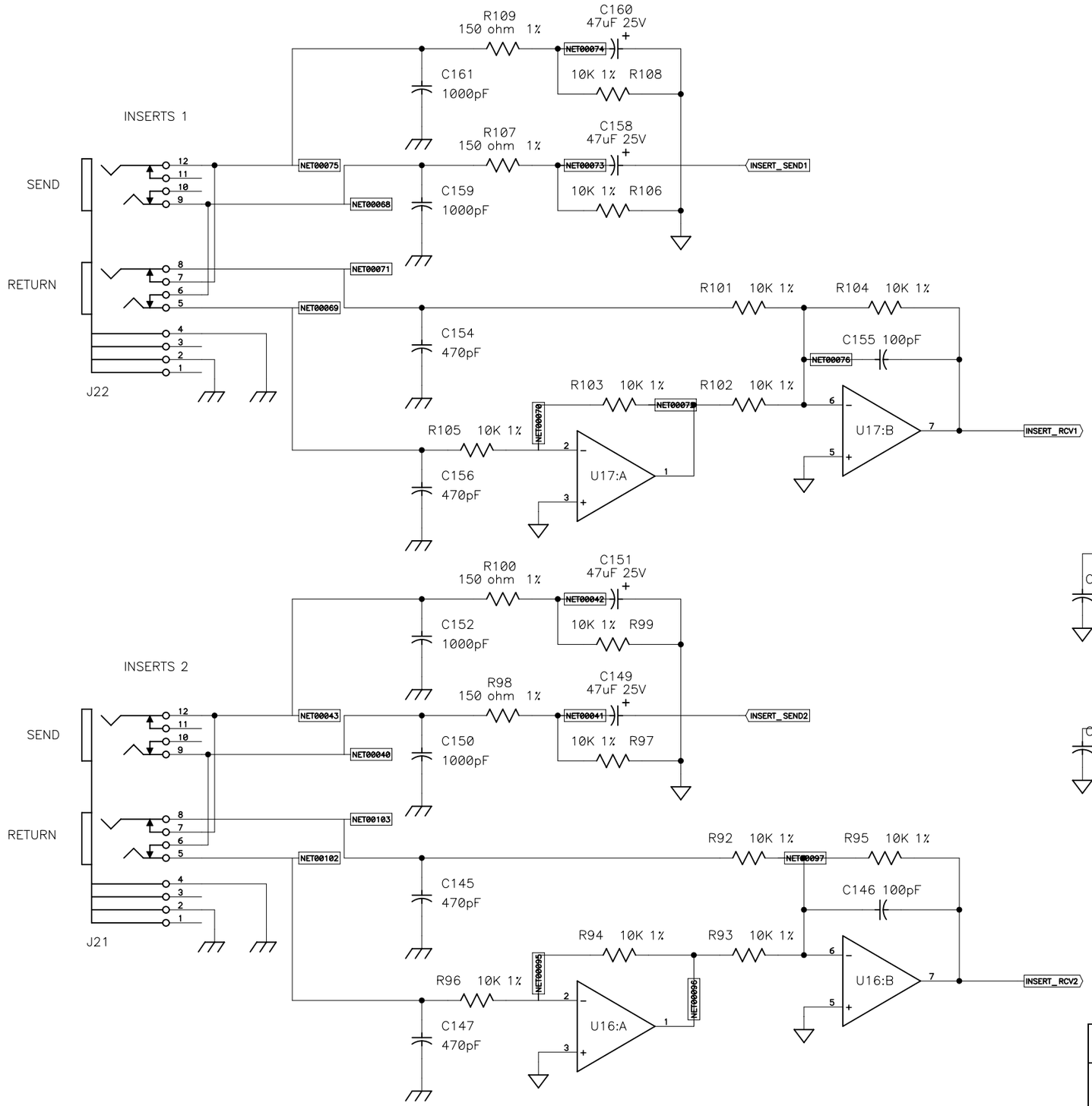

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TEST POINTS

TP72	NET00001
TP95	NET00002
TP239	NET00003
TP207	NET00004
TP68	NET00005
TP215	NET00006
TP24	NET00007
TP194	NET00008
TP226	NET00009
TP139	NET00010
TP235	NET00011
TP71	NET00012
TP146	NET00013
TP13	NET00014
TP69	NET00015
TP16	NET00016
TP1	NET00017
TP200	NET00018
TP212	NET00019
TP43	NET00020
TP51	NET00021
TP60	NET00022
TP211	NET00023
TP214	NET00024
TP274	NET00025
TP63	NET00026
TP66	NET00027
TP270	NET00028
TP84	NET00029
TP39	NET00030
TP41	NET00031
TP276	NET00032
TP268	NET00033
TP275	NET00034
TP26	NET00035
TP271	NET00036
TP273	NET00037
TP277	NET00038
TP25	NET00039
TP288	NET00040
TP285	NET00041
TP289	NET00042
TP290	NET00043
TP36	NET00044
TP4	NET00045
TP23	NET00046
TP227	NET00047
TP254	NET00048
TP250	NET00049
TP248	NET00050
TP255	NET00051
TP252	NET00052
TP249	NET00053
TP251	NET00054
TP257	NET00055
TP28	NET00056
TP27	NET00057
TP40	NET00058
TP113	NET00059
TP34	NET00060
TP19	NET00061
TP87	NET00062
TP5	NET00063
TP141	NET00064
TP83	NET00065
TP119	NET00066
TP140	NET00067
TP304	NET00068
TP302	NET00069
TP297	NET00070
TP303	NET00071
TP292	NET00072
TP296	NET00073
TP298	NET00074
TP305	NET00075
TP291	NET00076
TP70	NET00077
TP118	NET00078
TP185	NET00079
TP176	NET00080
TP221	NET00081
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TP219	NET00086
TP206	NET00087
TP199	NET00088
TP205	NET00089
TP217	NET00090
TP151	NET00091
TP238	NET00092
TP220	NET00093
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TP284	NET00095
TP282	NET00096
TP283	NET00097
TP130	NET00098
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TP286	NET00102
TP287	NET00103
TP62	NET00104
TP64	NET00105
TP10	NET00106
TP132	NET00107
TP108	NET00108
TP143	NET00109
TP161	NET00110
TP162	NET00111
TP266	NET00112
TP261	NET00113
TP65	NET00114
TP265	NET00115
TP260	NET00116
TP258	NET00117
TP263	NET00118
TP267	NET00119
TP262	NET00120
TP3	NET00121
TP15	NET00122
TP17	NET00123
TP11	NET00124
TP8	NET00125
TP12	NET00126
TP14	NET00127
TP7	NET00128
TP2	NET00129
TP184	NET00130
TP179	NET00131
TP228	NET00132
TP223	NET00133
TP174	NET00134
TP187	NET00135
TP241	NET00136
TP306	NET00143
TP307	NET00139
TP29	NET00153
TP42	NET00154
TP183	24.576MHZ
TP142	RESET
TP181	MEM_CLK
TP102	RE
TP96	WE
TP197	WAIT
TP204	CS_IO
TP202	CS_MEM
TP131	FLASH_CS
TP122	XILINX_CS
TP172	ADD2
TP164	ADD3
TP169	ADD4
TP159	ADD5
TP168	ADD6
TP157	ADD7
TP167	ADD8
TP160	ADD9
TP166	ADD10
TP158	ADD11
TP123	ADD12
TP124	ADD13
TP125	ADD14
TP133	ADD15
TP134	ADD16
TP136	ADD17
TP154	ADD18
TP101	ADD19
TP110	ADD20
TP156	ADD21
TP91	DATA0
TP94	DATA1
TP99	DATA2
TP109	DATA3
TP104	DATA4
TP129	DATA5
TP106	DATA6
TP107	DATA7
TP103	DATA8
TP86	DATA9
TP85	DATA10
TP127	DATA11
TP128	DATA12
TP89	DATA13
TP88	DATA14
TP92	DATA15
TP145	TPA2+
TP137	TPA2−
TP201	TPB2+
TP126	TPB2−
TP272	MON_OUT_A_R
TP264	MON_OUT_A_L
TP269	MON_OUT_B_R
TP259	MON_OUT_B_L
TP112	TPA1+
TP100	TPA1−
TP191	TPB1+
TP93	TPB1−
TP208	TPBIAS2
TP198	TPBIAS1
TP188	DSP_T1
TP178	AUD_RCV2
TP189	AUD_RCV1
TP186	AUD_XMIT2
TP152	AUD_XMIT1
TP229	DSP_TMS
TP243	DSP_TRST
TP222	DSP_TDO
TP231	DSP_TCK
TP230	DSP_TDI
TP232	DSP_EMU0
TP240	DSP_EMU1
TP190	RESET_LLC
TP175	RESET_ARM
TP245	ADC_DFS0
TP213	ICE_IRQ0
TP180	ICE_IRQ1
TP209	XIL_INT
TP75	SPDIF_SCK
TP210	SPDIF_TXD
TP193	SPDIF_RXD
TP171	FPGA_RESET
TP177	SPDIF_SEL
TP246	ADC_CKS0_DFS1
TP247	ADC_CKS1
TP138	CODEC_RESET
TP47	BCLK
TP48	WCLK
TP53	SDTO4
TP49	SDTO5
TP57	SDTO6
TP59	SDTO7
TP76	SDTO8
TP55	ADAT_OPT_INA
TP58	ADAT_OPT_OUTA
TP45	ADAT_OPT_OUTB
TP115	XIL_WORD_CLK
TP18	SPDIF_RCVR_MCLK
TP52	EXT_WORD_CLK_IN
TP33	SPDIF_DATA_OUT
TP50	SPDIF_DATA_IN
TP173	CLK1
TP182	FSYNC1
TP196	AUD_RCV5
TP192	AUD_RCV4
TP165	AUD_RCV3
TP155	AUD_XMIT5
TP150	AUD_XMIT4
TP153	AUD_XMIT3
TP170	CLK0
TP78	FPGA_MCLK
TP163	FSYNC0
TP35	SDTI6
TP37	SDTI5
TP20	SDTI4
TP21	SDTI3
TP38	SDTI2
TP22	SDTI1
TP46	SDTO1
TP44	SDTO2
TP54	SDTO3
TP295	INSERT_RCV1
TP301	INSERT_SEND1
TP294	INSERT_SEND2
TP300	INSERT_RCV2
TP299	TALKBACK_IN+
TP293	TALKBACK_IN−
TP77	LEV_POT_DATA
TP79	LEV_POT_CS
TP81	LEV_POT_CLK
TP80	BUTTON1
TP90	BUTTON3
TP97	BUTTON2
TP82	BUTTON5
TP105	BUTTON4
TP114	FP_CLK
TP121	FP_DATA
TP256	MIDI_IN2
TP253	MIDI_OUT2
TP224	MIDI_OUT1
TP244	MIDI_IN1
TP242	MIDI_OUT
TP225	MIDI_IN
TP31	CODEC_RESET
TP233	ARM_TMS
TP234	ARM_TDI
TP236	ARM_TDO
TP237	ARM_TCK
TP6	ADAT_OPT_INB
TP120	XIL_TMS
TP147	XIL_TCK
TP144	XIL_TDO
TP148	XIL_TDI
TP135	FP_UPDATE
TP61	NET00000
TP56	NET00137
TP32	NET00138
TP67	NET00140
TP73	NET00141
TP9	NET00142
TP30	NET00145
TP74	SDTO9
TP280	NET00147
TP278	NET00148
TP279	NET00149
TP281	NET00150
TP195	AUD_XMIT6
TP308	MCLK
TP309	ANA_MCLK
TP310	SPDIF_MCLK

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